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By Dan H. Wolaver

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Aug 30, 2012 This tutorial style video presents the basics of Phase Locked Loop circuits. A basic block diagram of a PLL is shown, and the individual blocks are

<http://www.youtube.com/watch?v=SS7z8WsXPMk>

A phase-locked loop Network Design; A phase-locked loop (PLL) is an electronic circuit with a voltage- or current-driven oscillator that is constantly

<http://searchnetworking.techtarget.com/definition/phase-locked-loop>

Phase locked loops A phase locked loop circuit operates by producing an oscillator frequency to match the frequency of an input Electronic circuit (IC) design;

<http://www.electronics-manufacturers.com/products/electrical-electronic-components/integrated-circuit-ic/phase-locked-loop-pll/>

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<http://www.radio-electronics.com/info/rf-technology-design/pll-synthesizers/phase-locked-loop-tutorial.php>

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tutorial about the PLL loop filter and its design loop filter is crucial to the operation of the whole phase locked loop. The choice of the circuit

<http://www.radio-electronics.com/info/rf-technology-design/pll-synthesizers/loop-filter-design.php>

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